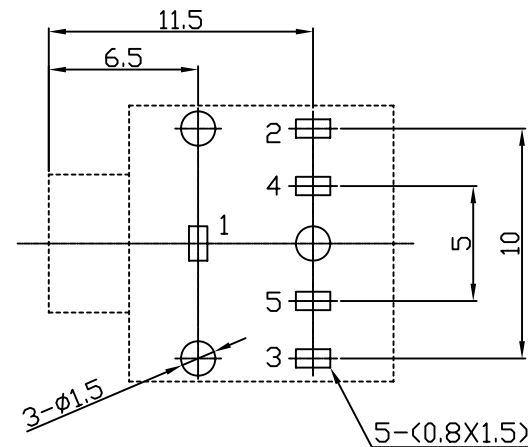
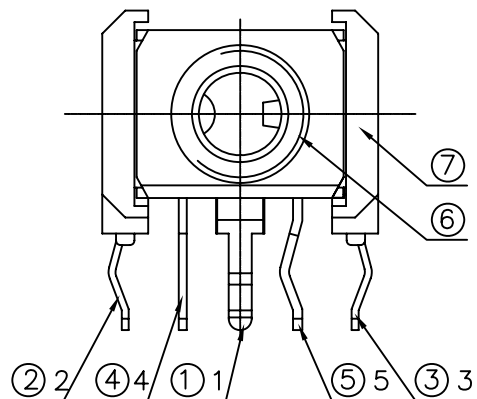
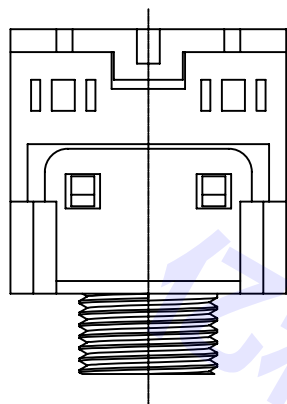
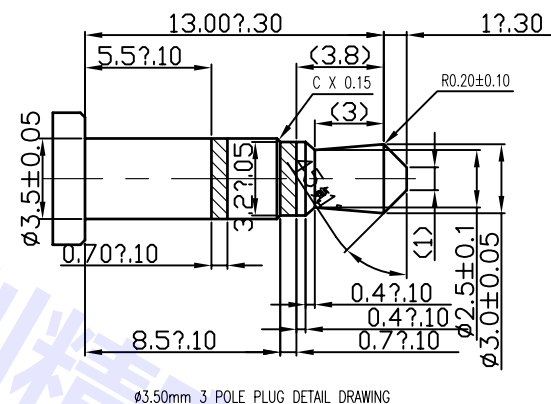
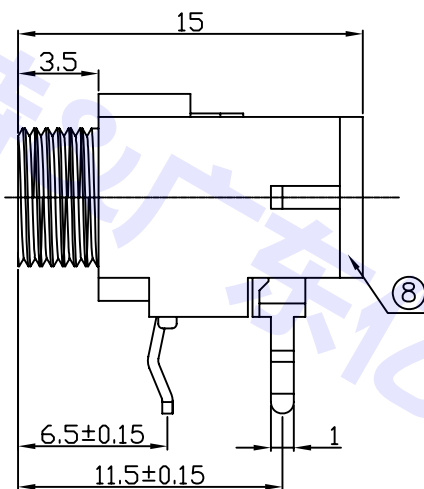
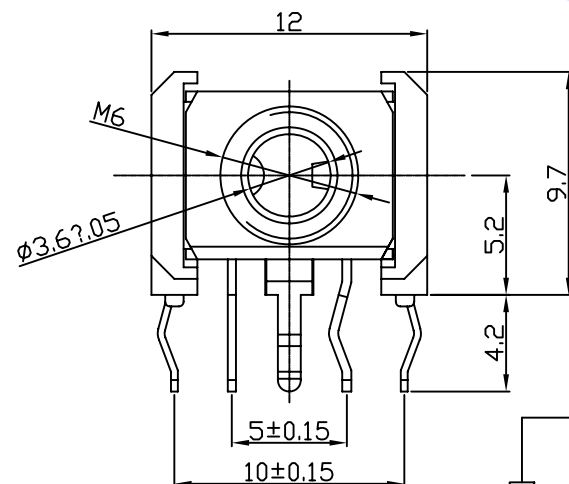
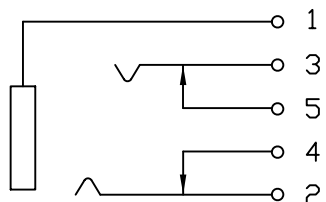




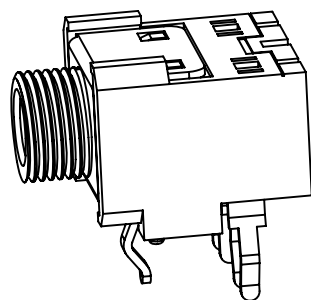
PCB LAYOUT TOP VIEW



φ3.50mm 3 POLE PLUG DETAIL DRAWING



CIRCUIT DIAGRAM



⑧	COVER PC-PJ-3047-Z ₂	1	PBT (BLACK)	
⑦	HOUSING PH-PJ-3048AS-Z ₂	1	PBT (BLACK)	
⑥	FIX TURN MS-PJ-3048AMS-Q4C	1	H59	CuSn-PLATED
⑤	TERMINAL MT-PJ-3048S-5Q3S	1	BRASS t=0.40	Ag-PLATED
④	TERMINAL MT-PJ-3048S-4Q3S	1	BRASS t=0.40	Ag-PLATED
③	TERMINAL MT-PJ-3048S-3Q1S	1	PHOSPHOR BRONZE t=0.30	Ag-PLATED
②	TERMINAL MT-PJ-3048S-2Q1S	1	PHOSPHOR BRONZE t=0.30	Ag-PLATED
①	TERMINAL MT-PJ-3048AS-1Q3S	1	BRASS t=0.45	Ag-PLATED
No.	PART NAME	QTY	MATERIAL (THICK, COLOR)	REMARK
DSND	Chuyunfeng	DATE	2009.09.22	SCALE: 3 : 1
CHKD		DATE		VIEW:
APVD		DATE		UNIT: mm
				NAME: EARPHONE JACK
				CAT. NO. PJ-3048AMS-Z ₂ S-Y RoHS

Marker	Describe	Data	DSND	
UNSPECIFIED TOLERANCE	≤4 ±0.15	4-16 ±0.20	≥16 ±0.30	ANGULAR ±5°

GDEL 广东亿利精密电子有限公司

DWN. NO. Q/XL-QW-001A-0111 B0